

# Electronic Filing System (EFS) Data **Electronic Patent Application Submission USPTO** Use Only

EFS ID:

63521

Application ID:

10749096

Title of Invention:

**MULTI-LAYER INTEGRATED** SEMICONDUCTOR STRUCTURE

HAVING AN ELECTRICAL

SHIELDING PORTION

First Named Inventor:

Rafael Reif

Domestic/Foreign Application: Domestic Application

Filing Date:

2003-12-30

Effective Receipt Date:

2004-06-29

**Submission Type:** 

Information Disclosure

Statement

Filing Type:

Confirmation number:

3177

Attorney Docket Number:

MIT-136DUS

**Total Fees Authorized:** 

Digital Certificate Holder: cn=Christopher S. Daly,ou=Registered Attorneys,ou=Patent and

Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US Certificate Message Digest: 696de19da1500851bf65218d971d83ffc96d64b0



#### **TRANSMITTAL**

Title of Invention

MULTI-LAYER INTEGRATED SEMICONDUCTOR STRUCTURE HAVING AN ELECTRICAL SHIELDING PORTION

Application Number:

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Submitted by:	Elec. Sign.	Sign. Capacity	
Christopher S Daly Registered Number: 37,303	/csd/	Attorney	

Documents being submitted

Files

us-ids

MIT-136DUS-usidst.xml

us-ids.dtd

us-ids.xsl

Comments



## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

MULTI-LAYER INTEGRATED SEMICONDUCTOR STRUCTURE HAVING AN ELECTRICAL SHIELDING **PORTION** 

Application Number:

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Rafael Reif Attorney Docket Number: MIT-136DUS

Art Unit:

3745

Search string:

( 4313126 or 4402761 or 4456888 or 4599704

or 4986861 or 5156997 or 5206186 or 5445994

or 5821138 or 5923087 or 6441478 ).pn.

### **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4313126	1982-01-26	Krumm et al.			
	2	4402761	1983-09-06	Feist			
	3	4456888	1984-01-26	Ayasli			
	4	4599704	1986-07-08	Mazin			
	5	4986861	1991-01-22	Nishida et al.			
	6	5156997	1992-10-20	Kumar et al.			
	7	5206186	1993-04-27	Neugebauer et al.			
	8	5445994	1995-08-29	Gilton			
	9	5821138	1998-10-13	Yamazaki et al.			
	10	5923087	1999-07-13	Suzuki et al.			
	11	6441478	2002-08-27	Park			

### Signature

Examiner Name	Date

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